

Minima Processor Oy was founded in April 2016 to develop sub-threshold-voltage processors based on technology developed by VTT Technical Research Centre and the Universities of Aalto and Turku. Minima was a founding partner in the RISC-V Foundation. Minima's techniques can be used to optimize processor voltage and frequency operating points dynamically, although the company also advocates the use of sub-threshold-voltage operation. Minima's clocking is adaptive-synchronous rather than asynchronous, and the company provides a middleware stack with its hardware that provides compatibility with dynamic voltage and frequency scaling (DVFS) regimes.

**[www.minimaprocessor.com](http://www.minimaprocessor.com)**

## **Minima Processor Oy**

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